		Application/Contr No.	Applicant(s)/Patent Under
N tice of Referenc s Cited		09/681,327	Reexamination SMITH ET AL.
		Examiner	Art Unit	
		Ted T. Vo	2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,339,840	01-2002	Kothari et al.	717/149
	B	US-6,314,560	11-2001	Dunn et al.	717/153
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chow et al., "Instruction Path Coprocessors", ACM, Proceedings of the 27th annual international symposium on Computer architecture, pages: 270-281, May 2000.
	V	Chow et al., "PipeRench Implementation of the Instruction Path Coprocessor", IEEE, pages: 147-158, December 2000.
	W	Patel et al., "rePLay: A Hardware Framework for Dynamic Program Optimization", CiteSeer, http://citeseer.nj.nec.com/patel99replay.html, pages: 1-26, December 1999.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.